

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10731566	
	Filing Date		2003-12-08	
	First Named Inventor	Gilbert C. Vandling		
	Art Unit	2825		
	Examiner Name	Vuthe Siek		
	Attorney Docket Number	1019.P003US		

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	1	P. GIRALD, "A Trace-Based Method for Delay Fault Diagnosis in Synchronous Sequential Circuit", IEEE, 1995 pages 526-532, Montpellier Cedex 05, France.	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

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